

Figure 1. Etch per cycle (EPC) vs bias (voltage mode). number of cycles: 100.

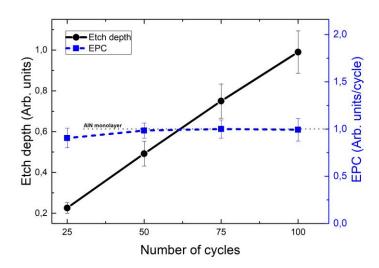


Figure 2. Etch depth and Etch per cycle vs Number of cycles.

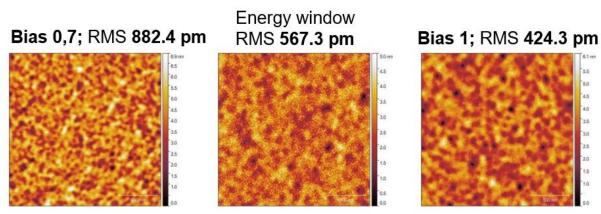


Figure 3. AIN RMS surface roughness for different bias voltages.